

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

4/30/04

Stylesheet Version v18.0

Title of Invention

METHOD AND SYSTEM FOR PERFORMING STATIC TIMING
ANALYSIS ON DIGITAL ELECTRONIC CIRCUITS

Application Number :

10/709 376

Confirmation Number:

First Named Applicant:

Steven Charlebois

Attorney Docket Number:

BUR920040056US1

Art Unit:

2825

Examiner:

Siek, V.

Search string:

(6237127 or 6308299).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
1	1	6237127	2001-05-22	Ted L. Craven et al.			
2	2	6308299	2001-10-23	Jerry R. Burch et al.			

Signature

Examiner Name	Date
VUTHE SIEK	8/8/06